

Day : Monday
Date: 2/12/2007

Time: 10:40:43

 **PALM INTRANET**

Inventor Information for 10/647064

| Inventor Name | City | State/Country |
|-----------------|------------|---------------|
| MORONEY, PAUL | OLIVENHAIN | CALIFORNIA |
| SPRUNK, ERIC J. | CARLSBAD | CALIFORNIA |

[Appln Info](#)[Contents](#)[Petition Info](#)[Atty/Agent Info](#)[Continuity/Reexam](#)[Foreign](#)Search Another: Application# or Patent# PCT / / or PG PUBS # Attorney Docket # Bar Code #

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|--|---|------------------|---------|------------------|
| L1 | 2 | "20040078584" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 10:39 |
| L2 | 12 | ((("5134700") or ("4864615") or ("4933898") or ("6532593") or ("6959089") or ("6961427"))).PN. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/12 10:59 |
| L3 | 4 | "20020150248" "20020164022" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:00 |
| L4 | 2 | ("6934389").PN. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/12 11:02 |
| L5 | 4 | ((("6236727") or ("6289455"))).PN. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/12 11:30 |
| L7 | 123 | (DVD and copying and illegal).ab. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:10 |
| L8 | 6 | L7 and ("1394" and interface).ab. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:04 |
| L9 | 17 | ("content scrambling system").ab. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:12 |

EAST Search History

| | | | | | | |
|-----|-----|---|---|----|-----|------------------|
| L10 | 2 | L9 and (decryption and copyrighted). ab. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:13 |
| L11 | 4 | ((("6236727") or ("6256391"))).PN. | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | OFF | 2007/02/12 12:24 |
| L12 | 48 | "6236727" "6256391" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:15 |
| L13 | 995 | 713/193.ccls. and @ad<"20020823" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:17 |
| L14 | 96 | 713/192.ccls. and @ad<"20020823" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:18 |
| L15 | 346 | 713/194.ccls. and @ad<"20020823" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:18 |
| L16 | 594 | 380/201.ccls. and @ad<"20020823" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:19 |
| L17 | 135 | 380/217.ccls. and @ad<"20020823" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:39 |

EAST Search History

| | | | | | | |
|-----|-------|--|---|----|----|------------------|
| L18 | 1902 | (L13 or L14 or L15 or L16 or L17) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:20 |
| L19 | 69 | "6236727" "6289455" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:21 |
| L20 | 54 | (L19 or L12) and @ad<"20020823" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:45 |
| L21 | 19 | L20 and L18 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:27 |
| L22 | 21 | "6289455" and @ad<"20020823" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:28 |
| L23 | 17 | L22 and (digital and content and key\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:28 |
| L24 | 26593 | (first or initial or primary) with (device or IC or microprocessor or smartcard or settop or (set with top) or interchip or (smart near2 card) or (integrat\$3 near3 chip) or micro or EEPROM or EPROM or RAM) with key\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:34 |
| L25 | 47550 | (encrypt\$3 or cipher\$3) with (engine or processor or computer or device or module) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:34 |

EAST Search History

| | | | | | | |
|-----|-------|---|---|----|----|------------------|
| L26 | 4537 | L24 and L25 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:34 |
| L27 | 39715 | (second or two or "next" or subsequent\$3 or another or follow\$3) with (device or IC or microprocessor or smartcard or settop or (set with top) or interchip or (smart near2 card) or (integrat\$3 near3 chip) or micro or EEPROM or EPROM or RAM) with key\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:35 |
| L28 | 127 | (digital with content) and "1394" and (((overwrit\$3 or rewritten or rewrote or delet\$3 or reuse) with (protect\$3 or guard\$3 or tamper\$3)) or (tamper with proof) with key\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:39 |
| L29 | 14 | L26 and L27 and L28 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:39 |
| L30 | 6 | L29 and @ad<"20020823" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:43 |
| L31 | 253 | (settop or (set near2 top)) with key\$3 with (decrypt\$3 or decrypt\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:49 |
| L32 | 29 | L31 and "1394" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:51 |
| L33 | 12 | L32 and @ad<"20020823" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:51 |

EAST Search History

| | | | | | | |
|-----|-----|---|---|----|----|------------------|
| L34 | 432 | (settop or (set near2 top) or DVR) with (key\$3 or CW) with (decrypt\$3 or encrypt\$3 or descrambl\$3) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 12:01 |
| L35 | 56 | L34 and "1394" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 11:51 |
| L36 | 24 | L35 and @ad<"20020823" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 12:02 |
| L37 | 270 | (settop or (set near2 top) or DVR) with (key\$3 or CW) with (decrypt\$3 or encrypt\$3 or descrambl\$3 or decod\$3 or decipher\$3) and (digital with content) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 12:02 |
| L38 | 109 | L37 and @ad<"20020823" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 12:10 |
| L40 | 14 | interchip and (digital with content) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 12:06 |
| L41 | 337 | (interchip or ((inner or inside or board) with (circuit or IC or microprocessor or smartcard or chip or micro or EEPROM or PROM or EPROM or RAM))) and (digital with content with (protect\$3 or guard\$3 or prevent\$3)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 12:09 |
| L42 | 140 | L41 and @ad<"20020823" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 12:10 |

EAST Search History

| | | | | | | |
|-----|----|-------------------|---|----|----|------------------|
| L43 | 20 | L42 and "1394" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 12:17 |
| L44 | 2 | "6782477" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/02/12 12:17 |
| L45 | 9 | ("6256391").URPN. | USPAT | OR | ON | 2007/02/12 12:24 |

[Sign in](#)

Google

[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [more »](#)

interchip digital content 1394

Search

[Advanced Search](#)
[Preferences](#)**Web**Results 1 - 10 of about 92 for **interchip digital content 1394**. (0.33 seconds)**INTERCHIP TRANSPORT BUS COPY PROTECTION - Patent EP1537465**A method for protecting **interchip content** pathways transporting **digital content** ... [0020]**Interchip** keys are also stored in the decoder 130, the IEEE-1394 ...www.freepatentsonline.com/EP1537465.html - 51k - [Cached](#) - [Similar pages](#)**Lexique**IDL §3 Interactive Distance Learning IDL §4 **Inter chip Digital Link** ... IEEE **1394**transaction layer ... IOCA § Image Object **Content** Architecture ...doc-telecom.enst-bretagne.fr/doc-telecom/resultat.jsp?l - 101k - [Cached](#) - [Similar pages](#)**[PDF] Silicon Graphics Tezro Visual White Paper**File Format: PDF/Adobe Acrobat - [View as HTML](#)text switch, or other **interchip** communication drawbacks. ... **digital content** creation, where additional screen space is especially important. ...www.sgi.com/pdfs/3533.pdf - [Similar pages](#)**Method for any speed dubbing using isochronous packets on ...**An Introduction to the **1394** High Performance Serial Bus", Michael Teener, CompCon92,Feb. 1992, pp. 316-321. "**Digital** Transmission **Content** Protection ...www.patentstorm.us/patents/7154910.html - 17k - [Cached](#) - [Similar pages](#)**Programmable first-in first-out (FIFO) memory buffer for ...****Inter-chip** bus with fair access for multiple data pipes ... "IEEE **1394**, The Cable Connection To Complete The **Digital** Revolution," Daniel Moore. ...www.patentstorm.us/patents/6904475.html - 50k - [Cached](#) - [Similar pages](#)**Computerworld Horizon Awards 2005 Honorees**Designed to handle rich **digital** media, it provides coordination of complex **content** processing flows, scheduling and load-balancing for **content** processing ...

www.computerworld.com/softwaretopics/software/story/0,10801,104421p5,00.html - 135k -

Feb 10, 2007 - [Cached](#) - [Similar pages](#)**eSP Glossary I**Description: **Inter-chip Digital Link**, a five-wire TDM interface defined by Motorola ...Description: The initial **content** of a linear feedback shift register ...www.xilinx.com/esp/glossary/i.htm - 94k - [Cached](#) - [Similar pages](#)**[PDF] Microsoft PowerPoint - Complete_new.ppt**File Format: PDF/Adobe Acrobat - [View as HTML](#)IDL (**Inter-chip Digital Link**): Motorola ... Increased Sharing of **digital content** inside the home ... Phoneline, powerline, Ethernet, IEEE-**1394**/Firewire, ...

www.xilinx.com/esp/consumer/home_networking/pdf_files/ch_3_ba/complete.pdf -

[Similar pages](#)**Jitter Testing for Gigabit Serial Communication Transceivers**A high-speed **digital** serial communication device's basic function is to transmit data ...Spectral **content** of phase noise that is above the PLL's natural ...doi.ieeecomputersociety.org/10.1109/54.980054 - [Similar pages](#)**Liste d'acronymes**

Information and **Content** Exchange ... Independent **Content** Provider. Initial Connection
Protocol ... **Interchip Digital** Link. Interface Definition Language ...
worldserver.oleane.com/heissler/acronyme/i.html - 143k - [Cached](#) - [Similar pages](#)

Result Page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) **[Next](#)**

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

©2007 Google

[Sign in](#)

Google

[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [more »](#)

bus copy digital content 1394

Search

[Advanced Search](#)
[Preferences](#)**Web**Results 1 - 10 of about **192,000** for **bus copy digital content 1394**. (0.62 seconds)**Newnex Technology — 1394 Technology Benefits**

The key technology for this **digital** traffic is the IEEE **1394** multimedia **bus**, the most reliable, cost-effective, and efficient way to move audio and video ...

www.newnex.com/tech/1394/benefits.php - 29k - [Cached](#) - [Similar pages](#)

1394 Trade Association: Technology

Has the **Digital** Video **copy** protection problem now been resolved? What about protection for **content** coming into the home or office? Does **1394** have a formal ...

www.1394ta.org/Technology/About/faq.htm - 33k - [Cached](#) - [Similar pages](#)

1394 Trade Association: Press

1394 is the standard that does protect illegal copying – without denying user rights. The **Digital** Transmission **Copy** Protection protocol (known as 5C) was ...

www.1394ta.org/Press/2002Press/july/7.a.htm - 18k - [Cached](#) - [Similar pages](#)

IEEE 1394 - The Multimedia Bus of the Future, Part 2

For transmitting **digital** AV **content** over the **1394 bus**, ... To implement such an encryption scheme, the **Copy** Protection Working Group (CPTWG) has considered ...

www.cablelabs.com/news/newsletter/SPECS/spectechoct/tech.pgs/leadstory.html - 21k - [Cached](#) - [Similar pages](#)

[PDF] 5C Digital Transmission Content Protection White Paper

File Format: PDF/Adobe Acrobat - [View as HTML](#)

1394 Content Protection Architecture. **Copy** Protection Layers ... **1394. Bus**. Encrypted. **Content**. Un-Encrypted. **Content**. AKE Protocol. & SRMs. **Content** ...

www.dtcp.com/data/wp_spec.pdf - [Similar pages](#)

[PDF] Matsushita Develops IEEE 1394 Single Chip LSI Supporting DTCP

File Format: PDF/Adobe Acrobat - [View as HTML](#)

[2] DTCP (**Digital** Transmission **Content** Protection). An IEEE **1394**-compliant **copy** protection technology proposed by 5 companies: Hitachi, Ltd., ...

www.dtcp.com/data/press/matsu.pdf - [Similar pages](#)

EnThink Introduces Robust 5C Digital Content Protection Technology ...

The IEEE **1394** serial **bus** is a connectivity standard that is an important ... for secure transmission of **copy**-protected **content** between **digital** devices. ...

findarticles.com/p/articles/mi_m0EIN/is_2000_June_19/ai_62792156 - 33k -

[Cached](#) - [Similar pages](#)

IDB-1394 Becomes First Digital Automotive Network Approved to ...

IDB-**1394** (Intelligent transportation systems Data **Bus** using IEEE1394 technology) ... **Content** Protection over IDB-**1394**), which was developed by the **Digital** ...

www.findarticles.com/p/articles/mi_m0EIN/is_2005_Jan_6/ai_n8684442 - 34k -

[Cached](#) - [Similar pages](#)

IEEE 1394 drives expand video-storage options - 9/25/2003 - EDN

Content providers and broadcasters are concerned about **copy** protection of data, especially data on **digital** interfaces, such as **1394**. ...

www.edn.com/article/CA323016.html - [Similar pages](#)

[PDF] **IEEE 1394** - Cable's home **digital** network interface of choice ...

File Format: PDF/Adobe Acrobat

versatile IEEE **1394** high-performance serial **bus** as the bridge between the diversity ...
provided by the **digital** transmission **content** protection ...

ieeexplore.ieee.org/iel5/6362/17019/00785156.pdf?arnumber=785156 - [Similar pages](#)

Result Page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) **[Next](#)**

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied?](#) [Help us improve](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

©2007 Google



Welcome United States Patent and Trademark Office

Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

Results for "((copy and protection and chip)<in>metadata)"

e-mail

Your search matched 21 of 1489021 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

» Search Options

[View Session History](#)[New Search](#)

» Other Resources

(Available For Purchase)

Top Book Results

[Intellectual Property Law for](#)[Engineers and Scientists](#)

by Rockman, H. B.;

Hardcover, Edition: 1

[View All 1 Result\(s\)](#)

» Key

IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

Modify Search

((copy and protection and chip)<in>metadata)

Search

☐ Check to search only within this results setDisplay Format: ☒ Citation ☐ Citation & Abstract [view selected items](#) [Select All](#) [Deselect All](#)

- ☐ 1. **Improving scratch-pad memory reliability through compiler-guided data t**
Li, F.; Chen, G.; Kandemir, M.; Kolcu, I.;
[Computer-Aided Design, 2005. ICCAD-2005. IEEE/ACM International Confere](#)
6-10 Nov. 2005 Page(s):1002 - 1005
Digital Object Identifier 10.1109/ICCAD.2005.1560208
[AbstractPlus](#) | Full Text: [PDF\(252 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 2. **Chip in disc for optical storage**
Akkermans, T.H.M.; Kahlman, J.A.H.;
[Optical Memory and Optical Data Storage Topical Meeting, 2002. International](#)
7-11 July 2002 Page(s):3 - 5
Digital Object Identifier 10.1109/OMODS.2002.1028548
[AbstractPlus](#) | Full Text: [PDF\(266 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 3. **Micro Law-the first chip-layout copying case**
Stern, R.H.;
[Micro, IEEE](#)
Volume 11, Issue 4, Aug. 1991 Page(s):3 - 6, 94
Digital Object Identifier 10.1109/40.85718
[AbstractPlus](#) | Full Text: [PDF\(452 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 4. **The law on reverse engineering**
Rauch, J.G.;
[Spectrum, IEEE](#)
Volume 30, Issue 8, Aug. 1993 Page(s):47 - 48
Digital Object Identifier 10.1109/6.223642
[AbstractPlus](#) | Full Text: [PDF\(264 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 5. **Direct access test scheme for IP core protection**
Yu-Cheng Fan; Hsueh-Yen Yang; Hen-Wai Tsao;
[Advanced System Integrated Circuits 2004. Proceedings of 2004 IEEE Asia-P;](#)
[on](#)
4-5 Aug. 2004 Page(s):262 - 265
Digital Object Identifier 10.1109/APASIC.2004.1349467

[AbstractPlus](#) | Full Text: [PDF\(400 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **6. Improving memory encryption performance in secure processors**
Jun Yang; Lan Gao; Youtao Zhang;
[Computers, IEEE Transactions on](#)
Volume 54, Issue 5, May 2005 Page(s):630 - 640
Digital Object Identifier 10.1109/TC.2005.80
[AbstractPlus](#) | Full Text: [PDF\(792 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **7. Localized watermarking: methodology and application to template mappi**
Kirovski, D.; Potkonjak, M.;
[Acoustics, Speech, and Signal Processing, 2000. ICASSP '00. Proceedings. 21](#)
[International Conference on](#)
Volume 6, 5-9 June 2000 Page(s):3235 - 3238 vol.6
Digital Object Identifier 10.1109/ICASSP.2000.860089
[AbstractPlus](#) | Full Text: [PDF\(440 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **8. Stego-signature at logic synthesis level for digital design IP protection**
Aijiao Cui; Chip-Hong Chang;
[Circuits and Systems, 2006. ISCAS 2006. Proceedings. 2006 IEEE Internation](#)
[21-24 May 2006 Page\(s\):4 pp.](#)
Digital Object Identifier 10.1109/ISCAS.2006.1693657
[AbstractPlus](#) | Full Text: [PDF\(3080 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **9. VLSI design exchange with intellectual property protection in FPGA envii**
both secret and public-key cryptography
Adi, W.; Ernst, R.; Soudan, B.; Hanoun, A.;
[Emerging VLSI Technologies and Architectures, 2006. IEEE Computer Society](#)
[Symposium on](#)
Volume 00, 2-3 March 2006 Page(s):6 pp.
Digital Object Identifier 10.1109/ISVLSI.2006.94
[AbstractPlus](#) | Full Text: [PDF\(224 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **10. Efficient memory integrity verification and encryption for secure process**
Suh, G.E.; Clarke, D.; Gasend, B.; van Dijk, M.; Devadas, S.;
[Microarchitecture, 2003. MICRO-36. Proceedings. 36th Annual IEEE/ACM Inte](#)
[Symposium on](#)
2003 Page(s):339 - 350
Digital Object Identifier 10.1109/MICRO.2003.1253207
[AbstractPlus](#) | Full Text: [PDF\(431 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **11. IP watermarking techniques: survey and comparison**
Abdel-Hamid, A.T.; Tahar, S.; El Mostapha Aboulhamid;
[System-on-Chip for Real-Time Applications, 2003. Proceedings. The 3rd IEEE](#)
[Workshop on](#)
30 June-2 July 2003 Page(s):60 - 65
[AbstractPlus](#) | Full Text: [PDF\(266 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **12. Reuse and protection of intellectual property in the SpecC system**
Domer, R.; Gajski, D.D.;
[Design Automation Conference, 2000. Proceedings of the ASP-DAC 2000. As](#)
[Pacific](#)

25-28 Jan. 2000 Page(s):49 - 54
Digital Object Identifier 10.1109/ASPdac.2000.835069
[AbstractPlus](#) | Full Text: [PDF](#)(500 KB) IEEE CNF
[Rights and Permissions](#)

- ☐ **13. Partial-encryption technique for intellectual property protection of FPGA-**
Kun-Wah Yip; Tung-Sang Ng;
[Consumer Electronics, IEEE Transactions on](#)
Volume 46, Issue 1, Feb. 2000 Page(s):183 - 190
Digital Object Identifier 10.1109/30.826397
[AbstractPlus](#) | Full Text: [PDF](#)(364 KB) IEEE JNL
[Rights and Permissions](#)

- ☐ **14. Increasing register file immunity to transient errors**
Memik, G.; Kandemir, M.T.; Ozturk, O.;
[Design, Automation and Test in Europe, 2005. Proceedings](#)
2005 Page(s):586 - 591 Vol. 1
Digital Object Identifier 10.1109/DATE.2005.181
[AbstractPlus](#) | Full Text: [PDF](#)(168 KB) IEEE CNF
[Rights and Permissions](#)

- ☐ **15. Fast secure processor for inhibiting software piracy and tampering**
Jun Yang; Youtao Zhang; Lan Gao;
[Microarchitecture, 2003. MICRO-36. Proceedings. 36th Annual IEEE/ACM International Symposium on](#)
2003 Page(s):351 - 360
Digital Object Identifier 10.1109/MICRO.2003.1253209
[AbstractPlus](#) | Full Text: [PDF](#)(385 KB) IEEE CNF
[Rights and Permissions](#)

- ☐ **16. VLSI design of an efficient embedded zerotree wavelet coder with functional watermarking**
Shen-Fu Hsiao; Yor-Chin Tai; Kai-Hsiang Chang;
[Consumer Electronics, IEEE Transactions on](#)
Volume 46, Issue 3, Aug. 2000 Page(s):628 - 636
Digital Object Identifier 10.1109/30.883423
[AbstractPlus](#) | Full Text: [PDF](#)(676 KB) IEEE JNL
[Rights and Permissions](#)

- ☐ **17. Hierarchical watermarking for protection of DSP filter cores**
Rashid, A.; Asher, J.; Mangione-Smith, W.H.; Potkonjak, M.;
[Custom Integrated Circuits, 1999. Proceedings of the IEEE 1999](#)
16-19 May 1999 Page(s):39 - 42
Digital Object Identifier 10.1109/CICC.1999.777240
[AbstractPlus](#) | Full Text: [PDF](#)(536 KB) IEEE CNF
[Rights and Permissions](#)

- ☐ **18. Dissemination activities within OMI technology networks**
Castro, M.; Cigarran, J.N.; de Mora, C.; Peire, J.; Riesgo, T.; Uceda, J.;
[Industrial Electronics, Control and Instrumentation, 1997. IECON 97. 23rd International Conference on](#)
Volume 1, 9-14 Nov. 1997 Page(s):213 - 217 vol.1
Digital Object Identifier 10.1109/IECON.1997.671049
[AbstractPlus](#) | Full Text: [PDF](#)(488 KB) IEEE CNF
[Rights and Permissions](#)

- ☐ **19. Wavelet-based digital watermarking with halftoning technique**
Leung, K.H.; Bing Zeng;
[Circuits and Systems, 2001. ISCAS 2001. The 2001 IEEE International Symposium on](#)

Volume 5, 6-9 May 2001 Page(s):235 - 238 vol. 5
Digital Object Identifier 10.1109/ISCAS.2001.922028

[AbstractPlus](#) | Full Text: [PDF](#)(352 KB) [IEEE CNF](#)
[Rights and Permissions](#)

- ☐ **20. Hardware architecture for data concealment using sub-band coding, LSE pseudo-random bit stream generators**
Adi, R.W.; Tio, C.M.M.; McLoughlin, I.V.;
[Circuits and Systems, 2000. IEEE APCCAS 2000. The 2000 IEEE Asia-Pacific 4-6 Dec. 2000 Page\(s\):159 - 162](#)
Digital Object Identifier 10.1109/APCCAS.2000.913431
[AbstractPlus](#) | Full Text: [PDF](#)(244 KB) [IEEE CNF](#)
[Rights and Permissions](#)
- ☐ **21. Performance measurements of a real-time digital watermarking system for monitoring**
Termont, P.; De Strycker, L.; Vandewege, J.; Haitsma, J.; Kalker, T.; Maes, M. Langell, A.; Alm, C.; Norman, P.;
[Multimedia Computing and Systems, 1999. IEEE International Conference on Volume 2, 7-11 June 1999 Page\(s\):220 - 224 vol.2](#)
Digital Object Identifier 10.1109/MMCS.1999.778284
[AbstractPlus](#) | Full Text: [PDF](#)(452 KB) [IEEE CNF](#)
[Rights and Permissions](#)

[Help](#) [Contact Us](#) [Privacy & :](#)

© Copyright 2006 IEEE –





Welcome United States Patent and Trademark Office

☐ Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

Results for "(((interchip and key)<in>metadata)) <and> (pyr >= 1950 <and> pyr <= 2002)"

☐ e-mail

Your search matched 6 of 1489021 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

» Search Options

[View Session History](#)
[New Search](#)

Modify Search

☐ Check to search only within this results set
Display Format: ☒ Citation ☐ Citation & Abstract

» Key

IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

 [Select All](#) [Deselect All](#)

- ☐ 1. **Average bit-error-rate performance of band-limited DS/SSMA communica**
 Joon Ho Cho; Jeong, Y.K.; Lehnert, J.S.;
[Communications, IEEE Transactions on](#)
 Volume 50, Issue 7, July 2002 Page(s):1150 - 1159
 Digital Object Identifier 10.1109/TCOMM.2002.800824
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(461 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 2. **A novel and efficient routing architecture for multi-FPGA systems**
 Khalid, M.A.S.; Rose, J.;
[Very Large Scale Integration \(VLSI\) Systems, IEEE Transactions on](#)
 Volume 8, Issue 1, Feb. 2000 Page(s):30 - 39
 Digital Object Identifier 10.1109/92.820759
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(172 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 3. **Performance evaluation for GMSK DS/SSMA systems based on a closed-**
for output SNR
 Park, S.; Park, S.-C.; Lee, K.;
[Electronics Letters](#)
 Volume 38, Issue 12, 6 June 2002 Page(s):599 - 600
[AbstractPlus](#) | Full Text: [PDF\(267 KB\)](#) IET JNL
- ☐ 4. **The effect of chip waveform on the performance of CDMA systems in mul**
noisy channels
 Anjaria, R.; Wyrwas, R.;
[Vehicular Technology Conference, 1992 IEEE 42nd](#)
 10-13 May 1992 Page(s):672 - 675 vol.2
 Digital Object Identifier 10.1109/VETEC.1992.245483
[AbstractPlus](#) | Full Text: [PDF\(312 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ 5. **3D optical interconnects for high-speed interchip and interboard commu**
 Louri, A.; Hongki Sung;
[Computer](#)
 Volume 27, Issue 10, Oct. 1994 Page(s):27 - 37
 Digital Object Identifier 10.1109/2.318581

[AbstractPlus](#) | Full Text: [PDF\(1192 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ 6. **Thermal design of an advanced multichip module for a RISC processor**
Garg, A.; Sham, T.-L.; Greub, H.; Loy, J.; McDonald, J.F.;
[Computer Design: VLSI in Computers and Processors, 1994. ICCD '94. Proceedings of the International Conference on](#)
10-12 Oct. 1994 Page(s):608 - 611
Digital Object Identifier 10.1109/ICCD.1994.331987
[AbstractPlus](#) | Full Text: [PDF\(364 KB\)](#) IEEE CNF
[Rights and Permissions](#)

Indexed by
 Inspec

[Help](#) [Contact Us](#) [Privacy & ;](#)

© Copyright 2006 IEEE –